	Application No.	Applicant(s)
	10/616,955	KANAMORI ET AL.
Notice of Allowability	Examiner	Art Unit
	Vu A. Le	2824
The MAILING DATE of this communication appear All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in or other appropriate communication is s	n this application. If not included unication will be mailed in due course. THIS
1. \boxtimes This communication is responsive to <u>an amendment filed or</u>	on 09/14/01.	
2. The allowed claim(s) is/are 11-15.	·	•
3. The drawings filed on 11 July 2003 are accepted by the Ex	aminer.	
 4.	been received. been received in Application cuments have been received of this communication to file IENT of this application. itted. Note the attached EXA cs reason(s) why the oath of the submitted. con's Patent Drawing Review s Amendment / Comment of the header according to 37 CF sit of BIOLOGICAL MATI	on No. 10/078,471. Id in this national stage application from the a reply complying with the requirements AMINER'S AMENDMENT or NOTICE OF declaration is deficient. If (PTO-948) attached If in the Office action of the drawings in the front (not the back) of R 1.121(d). ERIAL must be submitted. Note the
Attachment(s) 1. ☐ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	6. ☐ Interview S Paper No./ 8), 7. ☐ Examiner's	formal Patent Application (PTO-152) ummary (PTO-413), Mail Date Amendment/Comment Statement of Reasons for Allowance Vu A. Le Primary Examiner Art Unit: 2824

REASONS FOR ALLOWANCE

1. The following is an examiner's statement of reasons for allowance: the present invention relates to an ECC circuit for a flash memory device. The independent claims 11-12 recite an ECC circuit for a non-volatile memory to correct the inputted data by assigning a new address for the inputted data when a write error occurred and the inputted data can not be corrected and ending the write operation if the inputted data can be corrected. The PRIOR ART fails to disclose or suggest such an ECC circuit for a non-volatile memory device as described in the independent claims 11-12, therefore, claims 11-15 are in condition for allowance.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Vu A. Le whose telephone number is (571) 272-1871. The examiner can normally be reached on M-F (7:00-3:30).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richard Elms can be reached on (571) 272-1869. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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